	Тур	L #	Hits	Search Text	DBs	Time Stamp	en	De	Er ro rs
1	BRS	L1	49102	differential near2 amplif\$	1	2006/12/ 08 19:31			
2	BRS	L2	17515	pmos near2 transistor\$		2006/12/ 08 19:31			
3	BRS	L3	19923	nmos near2 transistor\$		2006/12/ 08 19:31			
4	BRS	L4	523794	bias\$		2006/12/ 08 19:31			
5	BRS	L5	228105 9	control or controlled or controller\$ or controlling		2006/12/ 08 19:31			
6	BRS	L6	1505	L1 and L2 and L3 and L4 and L5	1	2006/12/ 08 _. 19:31			
7	BRS	L7	193845 4	activat\$ or enabl\$ or on or switch\$		2006/12/ 08 19:31			
8	BRS	L8	1355	L6 and L7		2006/12/ 08 19:31			
9	BRS	L9	229	L8 and "330"/\$.ccls.		2006/12/ 08 19:31			
10	BRS	L1 0	256	<pre>complement\$ near2 differential near2 amplif\$</pre>	1	2006/12/ 08 19:31			
11	BRS	L1 1	8	L9 and L10		2006/12/ 08 19:32			
12	BRS	L1 2	1142	330/253.ccls.	1	2006/12/ 08 19:33			

	Тур е	L #	Hits	Search Text	DBs	Time Stamp	Co mm en ts	fi	Er ro rs
13	BRS	L1 3	869	330/257.ccls.		2006/12/ 08 19:34			
14	BRS	L1 4	96	330/269.ccls.		2006/12/ 08 19:34			
15	BRS	L1 5	789	330/261.ccls.		2006/12/ 08 19:34			
16	BRS	L1 6	4837	-	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/12/ 08 19:36			
17	BRS	L1 7.	54510	differential near2 (amplifier or amplifiers)	US- PGPU B; USOC R; EPO; JPO; DERW; IBM_ TDB	2006/12/ 08 19:36			

	Тур е	L #	Hits	Search Text	DBs	Time Stamp	Co mm en ts	De	Er ro rs
18	BRS	L1 8			US- PGPU B; USOC R; JPO; DERW ENT; IBM_ TDB	2006/12/ 08 19:36			
19	BRS	L1 9		pmos near2 (transistor or transistors)		2006/12/ 08 19:36			

	Тур	L #	Hits	Search Text	DBs	Time Stamp	en	Er ro r De fi ni ti on	Er ro rs
20	BRS	L2 0	371241	bias	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/12/ 08 19:36			
21	BRS		2	control or controlled or controller\$ or controller\$ or controlling	US- PGPU B; USOC R; EPO; JPO; DERW; IBM_ TDB				

	Тур	L #	Hits	Search Text	DBs	Time Stamp		Er ro rs
22	BRS	L2 2		L16 and L17 and L18 and L19 and L20 and L21	US- PGPU B; USOC R; EPO; JPO; DERW ENT; IBM_ TDB	2006/12/ 08 19:36		٥